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LM79XX Series 3-Terminal Negative Regulators

National Semiconductor

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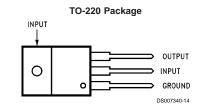
General Description

The LM79XX series of 3-terminal regulators is available with fixed output voltages of -5V, -12V, and -15V. These devices need only one external component—a compensation capacitor at the output. The LM79XX series is packaged in the TO-220 power package and is capable of supplying 1.5A of output current.

These regulators employ internal current limiting safe area protection and thermal shutdown for protection against virtually all overload conditions.

Low ground pin current of the LM79XX series allows output voltage to be easily boosted above the preset value with a

Connection Diagrams



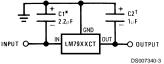
Front View Order Number LM7905CT, LM7912CT or LM7915CT See NS Package Number TO3B resistor divider. The low quiescent current drain of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode. For applications requiring other voltages, see LM137 data sheet.

Features

- Thermal, short circuit and safe area protection
- High ripple rejection
- 1.5A output current
- 4% tolerance on preset output voltage

Typical Applications





*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25 μ F aluminum electrolytic may be substituted.

 $\dagger Required$ for stability. For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100 $\mu\text{F},$ a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Voltage

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 $(V_o = -5V)$ $(V_o = -12V \text{ and } -15V)$

Input-Output Differential	
	0.51/
$(V_o = -5V)$	25V
$(V_o = -12V \text{ and } -15V)$	30V
Power Dissipation (Note 2)	Internally Limited
Operating Junction Temperature Range	0°C to +125°C
Storage Temperature Range	–65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	230°C

Electrical Characteristics

Conditions unless otherwise noted: I_{OUT} = 500 mA, C_{IN} = 2.2 µF, C_{OUT} = 1 µF, 0°C ≤ T_J ≤ +125°C, Power Dissipation ≤ 1.5W.

-25V

-35V

Part Number				LM7905C -5V				
Output Voltage Input Voltage (unless otherwise specified)				_				
Symbol	Parameter	Conditions	Min	Тур	Max			
Vo	Output Voltage	$T_J = 25^{\circ}C$	-4.8	-5.0	-5.2	V		
		$5 \text{ mA} \leq I_{OUT} \leq 1A$,	-4.75		-5.25	V		
		P ≤ 15W		$(-20 \le V_{IN} \le -7)$	')	V		
ΔV_O	Line Regulation	$T_{\rm J} = 25^{\circ}C$, (Note 3)		8	50	mV		
			$(-25 \le V_{IN} \le -7)$			V		
				2	15	mV		
			(−12 ≤ V _{IN} ≤ −8)			V		
ΔV_O	Load Regulation	T _J = 25°C, (Note 3)						
		$5 \text{ mA} \leq I_{OUT} \leq 1.5 \text{A}$		15	100	mV		
		250 mA ≤ I _{OUT} ≤ 750 mA		5	50	mV		
l _Q	Quiescent Current	$T_J = 25^{\circ}C$		1	2	mA		
ΔI_Q	Quiescent Current	With Line			0.5	mA		
	Change			$(-25 \le V_{IN} \le -7)$	7)	V		
		With Load, 5 mA $\leq I_{OUT} \leq 1A$			0.5	mA		
V _n	Output Noise Voltage	$T_A = 25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ Hz}$		125		μV		
	Ripple Rejection	f = 120 Hz	54	66		dB		
				$(-18 \le V_{IN} \le -8)$	3)	V		
	Dropout Voltage	$T_{\rm J} = 25^{\circ} {\rm C}, I_{\rm OUT} = 1{\rm A}$		1.1		V		
I _{OMAX}	Peak Output Current	$T_J = 25^{\circ}C$		2.2		A		
	Average Temperature	I _{OUT} = 5 mA,		0.4		mV/°C		
	Coefficient of	$0 \text{ C} \leq \text{T}_{\text{J}} \leq 100^{\circ}\text{C}$						
	Output Voltage							

Electrical Characteristics

Conditions unless otherwise noted: I_{OUT} = 500 mA, C_{IN} = 2.2 μ F, C_{OUT} = 1 μ F, 0°C \leq T_J \leq +125°C, Power Dissipation \leq 1.5W.

Part Number		LM7912C		С	LM7915C			Units	
Output Voltage		-12V			-15V				
	Input Voltage (unless otherwise specified) -1		-19V		-23V				
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25^{\circ}C$	-11.5	-12.0	-12.5	-14.4	-15.0	-15.6	V
		$5 \text{ mA} \leq I_{OUT} \leq 1A$,	-11.4		-12.6	-14.25		-15.75	V
		P ≤ 15W	(-27	$\leq V_{IN} \leq$	–14.5)	(-30	$\leq V_{IN} \leq$	–17.5)	V
ΔV_{O}	Line Regulation	$T_{J} = 25^{\circ}C$, (Note 3)		5	80		5	100	mV
			(-30	$\leq V_{IN} \leq$	-14.5)	(-30	$\leq V_{IN} \leq -$	-17.5)	V
				3	30		3	50	mV
			(-22	$\leq V_{IN} \leq$	–16)	(-26	$\delta \leq V_{IN} \leq$	≦ – 20)	V

Part Number Output Voltage			LM7912C		LM7915C			Units		
			–12V			–15V				
	Input Voltage (unless o	otherwise specified)		–19V		–23V				
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max		
ΔV_O	Load Regulation	$T_{J} = 25^{\circ}C$, (Note 3)								
		$5 \text{ mA} \leq I_{OUT} \leq 1.5 \text{A}$		15	200		15	200	mV	
		$250 \text{ mA} \le I_{OUT} \le 750 \text{ mA}$		5	75		5	75	mV	
l _q	Quiescent Current	$T_J = 25^{\circ}C$		1.5	3		1.5	3	mA	
Δl _Q	Quiescent Current	With Line			0.5			0.5	mA	
	Change		$(-30 \le V_{IN} \le -14.5)$		(–30 ≤V _{IN} ≤ –17.5)		17.5)	V		
		With Load, 5 mA $\leq I_{OUT} \leq 1A$			0.5			0.5	mA	
V _n	Output Noise Voltage	$T_A = 25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ Hz}$		300			375		μV	
	Ripple Rejection	f = 120 Hz	54	70		54	70		dB	
			(–25	$\leq V_{\rm IN} \leq$	–15)	(–30	$\leq V_{IN} \leq -$	-17.5)	V	
	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{OUT} = 1A$		1.1			1.1		V	
I _{OMAX}	Peak Output Current	$T_J = 25^{\circ}C$		2.2			2.2		A	
	Average Temperature	I _{OUT} = 5 mA,		-0.8			-1.0		mV/°C	
	Coefficient of	$0 \text{ C} \leq \text{T}_{\text{J}} \leq 100^{\circ}\text{C}$								
	Output Voltage									

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee Specific Performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. Note 2: Refer to Typical Performance Characteristics and Design Considerations for details.

Note 3: Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

Design Considerations

The LM79XX fixed voltage regulator series has thermal overload protection from excessive power dissipation, internal short circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature ($125^{\circ}C$) in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

	Тур	Max	Тур	Max
Package	θ _{JC}	θ _{JC}	θ_{JA}	θ_{JA}
	°C/W	°C/W	°C/W	°C/W
TO-220	3.0	5.0	60	40

$$P_{D MAX} = \frac{T_{J Max} - T_{A}}{\theta_{JC} + \theta_{CA}} \text{ or } \frac{T_{J Max} T_{A}}{\theta_{JA}}$$

$$\theta_{CA} = \theta_{CS} + \theta_{SA}$$
 (without heat sink)

Solving for T_J:

 $T_J = T_A + P_D (\theta_{JC} + \theta_{CA})$ or

= $T_A + P_D \theta_{JA}$ (without heat sink)

Where:

- T_J = Junction Temperature
- T_A = Ambient Temperature

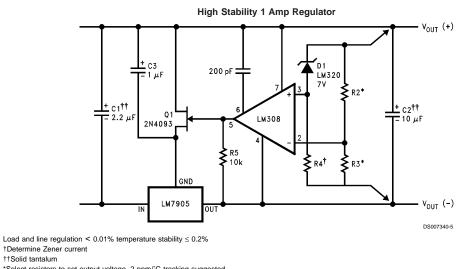
P_D = Power Dissipation

- θ_{JA} = Junction-to-Ambient Thermal Resistance
- θ_{JC} = Junction-to-Case Thermal Resistance
- θ_{CA} = Case-to-Ambient Thermal Resistance
- θ_{CS} = Case-to-Heat Sink Thermal Resistance
- θ_{SA} = Heat Sink-to-Ambient Thermal Resistance

Typical Applications

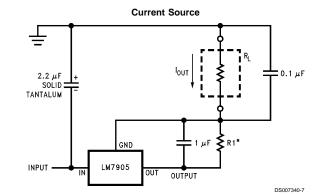
Bypass capacitors are necessary for stable operation of the LM79XX series of regulators over the input voltage and output current ranges. Output bypass capacitors will improve the transient response by the regulator.

The bypass capacitors, (2.2 μF on the input, 1.0 μF on the output) should be ceramic or solid tantalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be 10 μF or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.

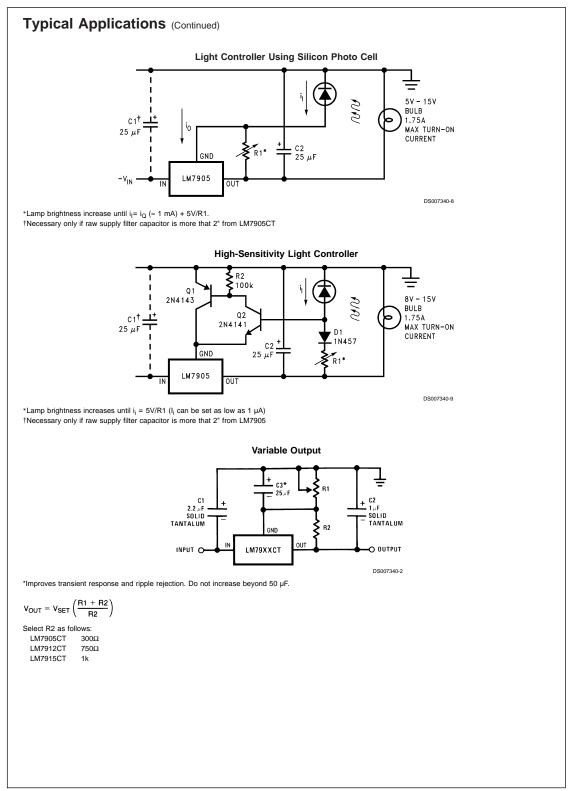


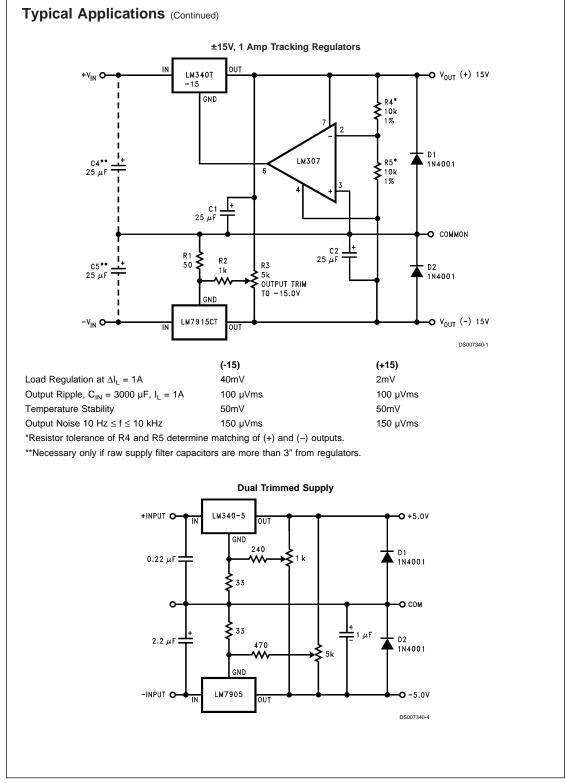
†Determine Zener current

*Select resistors to set output voltage. 2 ppm/°C tracking suggested

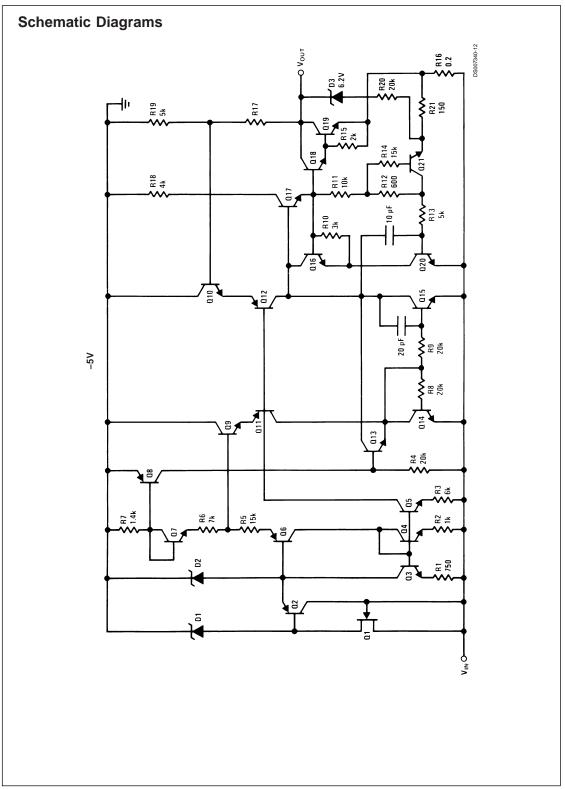


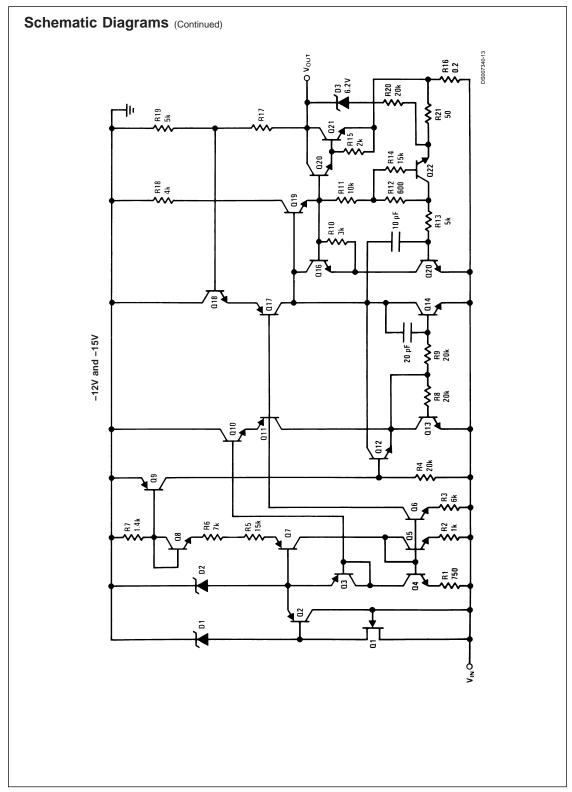
 $*I_{OUT} = 1 \text{ mA} + \frac{5V}{R1}$





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